



Substitute for form 1449A/PTO				Complete If Known	
<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b> (use as many sheets as necessary)				Application Number	09/994,982
				Filing Date	November 26, 2001
				First Named Inventor	Xia Dai
				Art Unit	2116
				Examiner Name	Yanchus III, Paul B.
Sheet	1	of	2	Attorney Docket Number	42390P9724X

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No.*	Document Number	Publication Date or Issue Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number - Kind Code* (if known)			
[Signature]		US-6,311,281	10-30-2001	Pole et al.	
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FOREIGN PATENT DOCUMENTS						
Examiner Initials*	Cite No.*	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T*
		Country Code* - Number* - Kind Code* (if known)				
[Signature]		JP 110115658 A	01-22-1999	Morikawa et al.		

Examiner Signature	[Signature]	Date Considered	5/18/06
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Based on PTO/SB/08A (09-03) as modified by Biskaly, Sotzkoff, Taylor & Zafman (wtr) 09/11/2003.

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				Art Unit	2116
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Sheet	2	of	2	Attorney Docket Number	42390P9724X

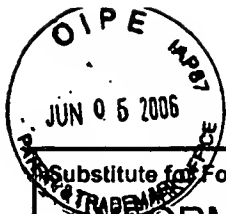
NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No.†	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T‡
K/S		"Power Management Clock Change for 603 Processor", IBM Technical Disclosure Bulletin, December 1, 1995, Vol. 38, No. 12, Pages 325-328.	

Examiner Signature		Date Considered	10/13/06
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Substitute for Form 1449/PTO

**INFORMATION DISCLOSURE  
STATEMENT BY APPLICANT**

(use as many sheets as necessary)

**Complete if Known**

Application Number	09/994,982
Filing Date	11/26/2001
First Named Inventor:	Xia Dei
Art Unit	2116
Examiner Name	Yanchus III, Paul B.
Attorney Docket Number	42P9724X

Sheet

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of

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**U.S. PATENT DOCUMENTS**

Examiner Initials*	Cite No. <sup>1</sup>	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code <sup>2</sup> (if known)			
		US- 6,041,480 B1	09-06-2005	Dai	

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Examiner Initials*	Cite No. <sup>1</sup>	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T <sup>2</sup>
		Country Code <sup>3</sup> Number <sup>4</sup> Kind Code <sup>5</sup> (if known)				
BB		JP 11-015658	01-22-1999	Matsushita Electric Ind Co Ltd		

**NON PATENT LITERATURE DOCUMENTS**

Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	T <sup>2</sup>
BB		BELLAOUAR, A., et al., "Supply Voltage Scaling for Temperature Insensitive CMOS Circuit Operation", 1057-7130/98 Copyright 1998 IEEE Transactions on Circuits and Systems-II: Analog and Digital Signal Processing, Vol. 45, No. 3, March 1998, pages 415-417.	
BB		DANCY, Abram, et al., "Techniques for Aggressive Supply Voltage Scaling and Efficient Regulation", 0-7803-3669-0 Copyright 1997 IEEE, IEEE 1997 Custom Integrated Circuits Conference, page 579-586.	
BB		GONZALEZ, Ricardo, et al., "Supply and Threshold Voltage Scaling for Low Power CMOS", 0018-9200/97 Copyright 1997 IEEE, IEEE Journal of Solid-State Circuits, Vol. 32, No. 8, August 1997, pages 1210-1216.	

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